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JC02 Rec'd PCT/PTO 19 MAR 2002

XA-9593

PATENT APPLICATION



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Takashi KOBAYASHI et al.

Appln. No.: 10/031,117

Group Art Unit: 2812

Filed: January 16, 2002

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND PROCESS
FOR PRODUCING THE SAME

* * *

REQUEST FOR CORRECTED FILING RECEIPT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

It is respectfully requested that the official filing
receipt be corrected as shown in red on the attached copy.
Supporting documentation is attached.

Respectfully submitted,

By: *Mitchell W. Shapiro*
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March 19, 2002

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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/031,117	01/16/2002	2812	3132	XA-9593	23	46	6

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CONFIRMATION NO. 3823

FILING RECEIPT



OC000000007586995

Date Mailed: 03/06/2002

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Applicant(s)

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Domestic Priority data as claimed by applicant

THIS APPLICATION IS A 371 OF PCT/JP00/06146 09/08/2000

Foreign Applications

JAPAN 11-257990 09/10/1999

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Projected Publication Date: Not Applicable, filed prior to November 29,2000

Non-Publication Request: No

Early Publication Request: No

Title

Integrated circuit and method of manufacture thereof.

Semiconductor Integrated Circuit Device and Process
for Producing the Same

Preliminary Class

438

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